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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/783,886	
	Filing Date	2/20/2004	
	First Named Inventor	Om P Agrawal	
	Art Unit	2819	
	Examiner Name	Don P. Le	
Total Number of Pages In This Submission	2	Attorney Docket Number	M-15311 US


ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Lattice Semiconductor Corporation		
Signature			
Printed name	Mark L. Becker		
Date	4/25/06	Reg. No.	31325

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Om P. Agrawal
Application No.: 10/783,886
Filed: 2/20/2004
For: Upgradeable and Reconfigurable Programmable Logic Device
Examiner: Don P. Le
Art Unit: 2819
Attorney Docket No.: M-15311 US

APPLICANTS' INTERVIEW SUMMARY STATEMENT

The substance of the April 12 interview between the undersigned and the Examiner was USPN 6,828,823 and how it differed from the application claims.

In the interview applicants argued that the claims as described in the remarks in the Response After Final Rejection were patentable over the '823 patent. The examiner acknowledged the argument but expressed concern that the use of "or" and "either" in the claims might still be interpreted as making the claimed data transfer capability optional for the two data ports. To meet that concern, applicants agreed to change the claim language as the examiner desired through an Examiner's Amendment.

Applicants maintain, however, that the changes appearing in the Examiner's Amendment are merely to the form of the claims and not to their substance, and that these changes thus do not narrow the scope of the claims as they appeared in the Response After Final Rejection.

Respectfully submitted,

Date: 4/25/06By: 

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